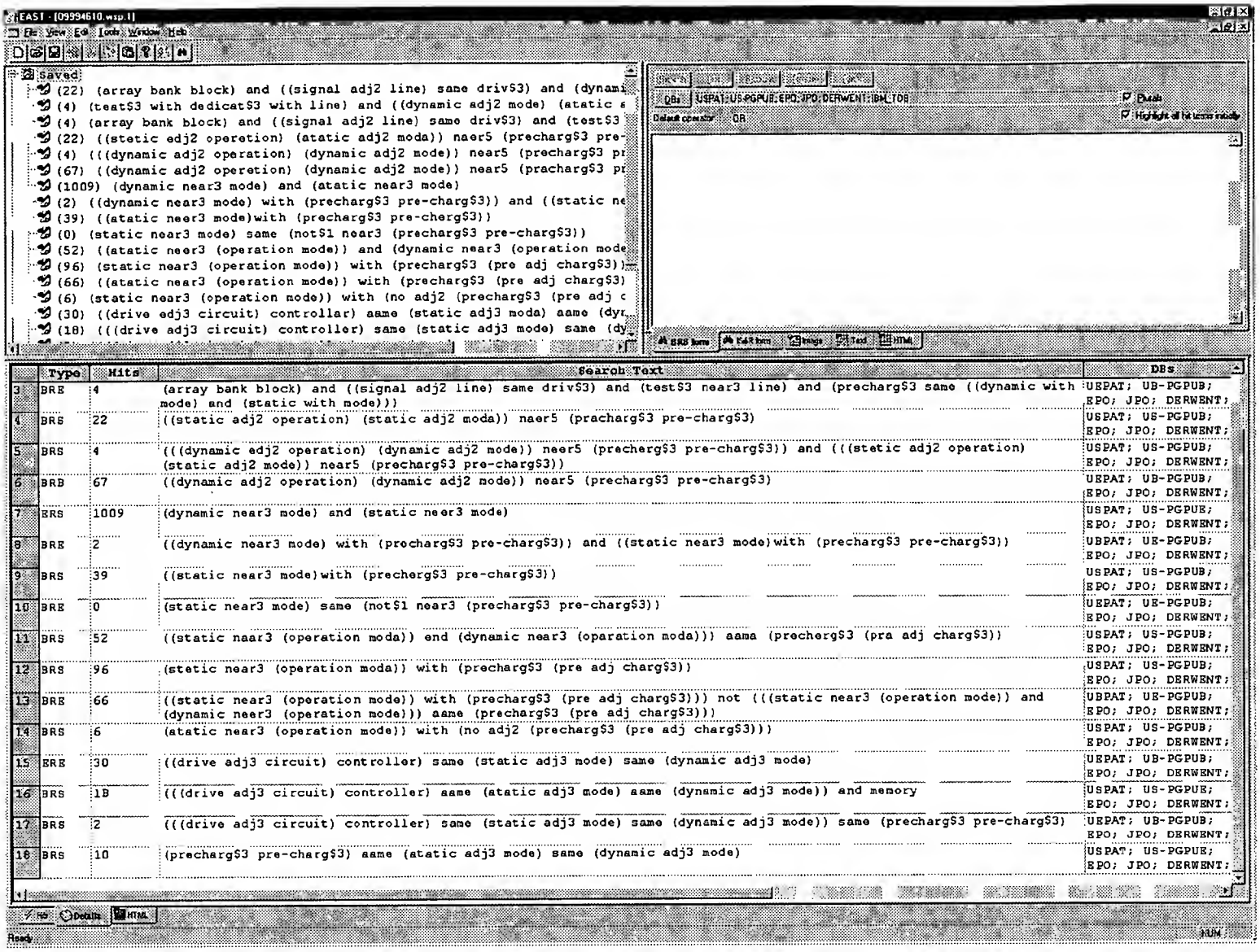
EAST - [09994610.wsp.1]
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Saved
(22) (array bank block) and ((signal adj2 line) same driv$3) and (dynami
(4) (teat$3 with dedicat$3 with line) and ((dynamic adj2 mode) (atatic a
(4) (array bank block) and ((signal adj2 line) same driv$3) and (test$3
(22) ((stetic edj2 operetion) (atatic adj2 moda)) naer5 (prechargS3 pre-
(4) (((dynamic adj2 operation) (dynamic adj2 mode)) near5 (prechargS3 pr
(67) ((dynamic adj2 operetion) (dynamic adj2 mode)) near5 (pracharg$3 pr
(1009) (dynamic near3 mode) and (atatic near3 mode)
(2) ((dynamic near3 mode) with (prechargS3 pre-chargS3)) and ((static ne
(39) ((atatic neer3 mode)with (prechargS3 pre-cherg$3))
(0) (static near3 mode) same (not$1 near3 (prechargS3 pre-chargS3))
(52) ((atatic neer3 (operation mode)) and (dynamic near3 (operation mode
(96) (static near3 (operation mode)) with (prechargS3 (pre adj chargS3))
(66) ((atatic near3 (operation mode)) with (prechargS3 (pre adj chargS3)
(6) (static near3 (operation mode)) with (no adj2 (prechargS3 (pre adj c
(30) ((drive edj3 circuit) controllar) aame (static adj3 moda) aame (dyr
(18) (((drive adj3 circuit) controller) same (static adj3 mode) same (dy
DBs USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
Default operator OR
Dups
Highlight all hit terms initially
ERR Items
Text
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Hits
Search Text
DBs
3 BRE 4 (array bank block) and ((signal adj2 line) same driv$3) and (test$3 near3 line) and (prechargS3 same ((dynamic with mode) and (static with mode))) USPAT; US-PGPUB; EPO; JPO; DERWENT;
4 BRS 22 ((static adj2 operation) (static adj2 moda)) naer5 (pracharg$3 pre-chargS3) USPAT; US-PGPUB; EPO; JPO; DERWENT;
5 BRS 4 (((dynamic edj2 operation) (dynamic adj2 mode)) neer5 (precherg$3 pre-chargS3)) and (((stetic adj2 operation) (static adj2 mode)) near5 (prechargS3 pre-chargS3)) USPAT; US-PGPUB; EPO; JPO; DERWENT;
6 BRS 67 ((dynamic adj2 operation) (dynamic adj2 mode)) near5 (precharg$3 pre-chargS3) USPAT; US-PGPUB; EPO; JPO; DERWENT;
7 BRS 1009 (dynamic near3 mode) and (static neer3 mode) USPAT; US-PGPUB; EPO; JPO; DERWENT;
8 BRS 2 ((dynamic near3 mode) with (prechargS3 pre-chargS3)) and ((static near3 mode)with (prechargS3 pre-chargS3)) USPAT; US-PGPUB; EPO; JPO; DERWENT;
9 BRS 39 ((static near3 mode)with (precherg$3 pre-chargS3)) USPAT; US-PGPUB; EPO; JPO; DERWENT;
10 BRS 0 (static near3 mode) same (not$1 near3 (prechargS3 pre-chargS3)) USPAT; US-PGPUB; EPO; JPO; DERWENT;
11 BRS 52 ((static naar3 (operation moda)) end (dynamic near3 (oparation moda))) aama (precherg$3 (pra adj chargS3)) USPAT; US-PGPUB; EPO; JPO; DERWENT;
12 BRS 96 (stetic near3 (operation moda)) with (prechargS3 (pre adj chargS3)) USPAT; US-PGPUB; EPO; JPO; DERWENT;
13 BRS 66 ((static near3 (operation mode)) with (prechargS3 (pre adj chargS3))) not (((static near3 (operation mode)) and (dynamic neer3 (operation mode))) aame (prechargS3 (pre adj chargS3))) USPAT; US-PGPUB; EPO; JPO; DERWENT;
14 BRS 6 (atatic near3 (operation mode)) with (no adj2 (prechargS3 (pre adj chargS3))) USPAT; US-PGPUB; EPO; JPO; DERWENT;
15 BRS 30 ((drive adj3 circuit) controller) same (static adj3 mode) same (dynamic adj3 mode) USPAT; US-PGPUB; EPO; JPO; DERWENT;
16 BRS 18 (((drive adj3 circuit) controller) aame (atatic adj3 mode) aame (dynamic adj3 mode)) and memory USPAT; US-PGPUB; EPO; JPO; DERWENT;
17 BRS 2 (((drive adj3 circuit) controller) same (static adj3 mode) same (dynamic adj3 mode)) same (prechargS3 pre-chargS3) USPAT; US-PGPUB; EPO; JPO; DERWENT;
18 BRS 10 (precharg$3 pre-chargS3) aame (atatic adj3 mode) same (dynamic adj3 mode) USPAT; US-PGPUB; EPO; JPO; DERWENT;
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